



VCCINT = 5.0V (6016)

VCCIO = 5V

Vih 2.0V min, 5.3V max  
Vil 0.3V min, 0.8V max

Voh 2.4V min  
Vol 0.45 V max

VCCIO = 3.3V

Vih 2.0V min, 3.3V max  
Vil 0.3V min, 0.8V max

Voh 2.4V TTL min, 3.1V ocmos min  
Vol 0.45V TTL max, 0.2V ocmos min

VCCINT = 3.3V (6016A)

VCCIO = 3.3V

Vih 1.7V min, 5.3V max  
Vil 0.3V min, 0.8V max

Voh 2.4V TTL min, 3.1V ocmos min  
Vol 0.45V TTL max, 0.2V ocmos min

User I/O after config if not used for chip-selecting function

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

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User I/O after configuration

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User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

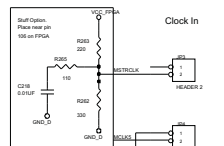
User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration



Clock In

Clock Out

STATUS

MCU1

MCU2

MCU3

MCU4

MCU5

MCU6

MCU7

MCU8

MCU9

MCU10

MCU11

MCU12

MCU13

MCU14

MCU15

MCU16

MCU17

MCU18

MCU19

MCU20

MCU21

MCU22

MCU23

MCU24

MCU25

MCU26

MCU27

MCU28

MCU29

MCU30

MCU31

MCU32

MCU33

MCU34

MCU35

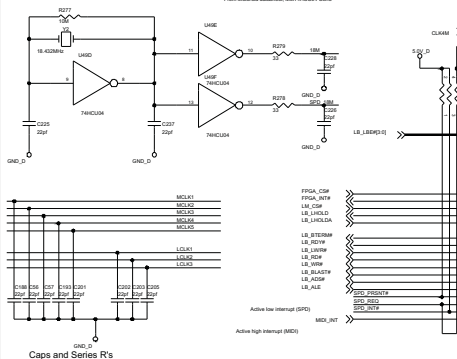
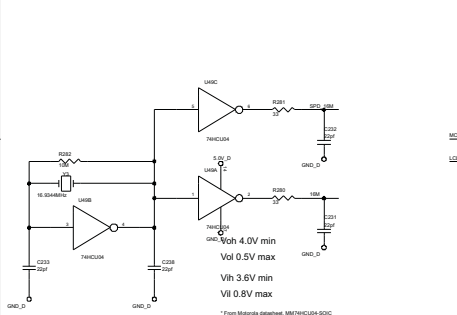
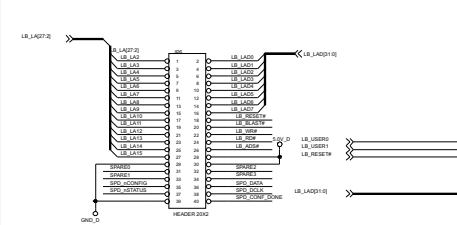
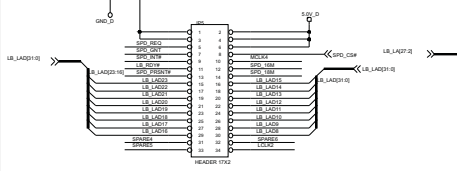
MCU36

MCU37

MCU38

MCU39

MCU40



VCCIO = 5V

Vih 2.0V min, 5.3V max  
Vil 0.3V min, 0.8V max

Voh 2.4V min  
Vol 0.45 V max

VCCIO = 3.3V

Vih 2.0V min, 3.3V max  
Vil 0.3V min, 0.8V max

Voh 2.4V TTL min, 3.1V ocmos min  
Vol 0.45V TTL max, 0.2V ocmos min

VCCINT = 3.3V (6016A)

VCCIO = 3.3V

Vih 1.7V min, 5.3V max  
Vil 0.3V min, 0.8V max

Voh 2.4V TTL min, 3.1V ocmos min  
Vol 0.45V TTL max, 0.2V ocmos min

User I/O after config if not used for chip-selecting function

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

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User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

User I/O after configuration

Clock In

Clock Out

STATUS

MCU1

MCU2

MCU3

MCU4

MCU5

MCU6

MCU7

MCU8

MCU9

MCU10

MCU11

MCU12

MCU13

MCU14

MCU15

MCU16

MCU17

MCU18

MCU19

MCU20

MCU21

MCU22

MCU23

MCU24

MCU25

MCU26

MCU27

MCU28

MCU29

MCU30

MCU31

MCU32

MCU33

MCU34

MCU35

MCU36

MCU37

MCU38

MCU39

MCU40

Active low interrupt (SPD)

Active high interrupt (SPD)

SPD\_GNT

SPD\_GNT

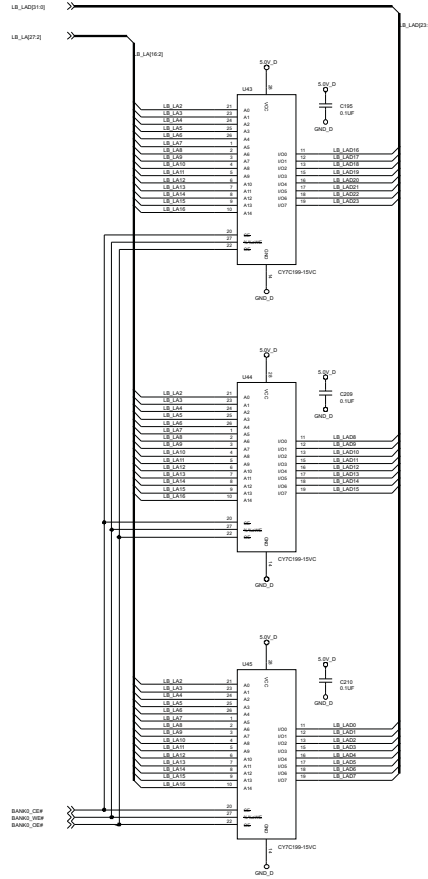
Cap's and Series R's go near source

Cap's and Series R's go near source

Cap's and Series R's go near source

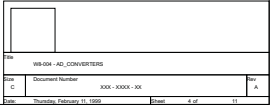
Cap's and Series R's go near source

V<sub>ih</sub> 2.2V min, 5.2V max  
V<sub>il</sub> 0.2V min, 0.2V max  
V<sub>oh</sub> 2.4V min  
V<sub>ol</sub> 0.4V max

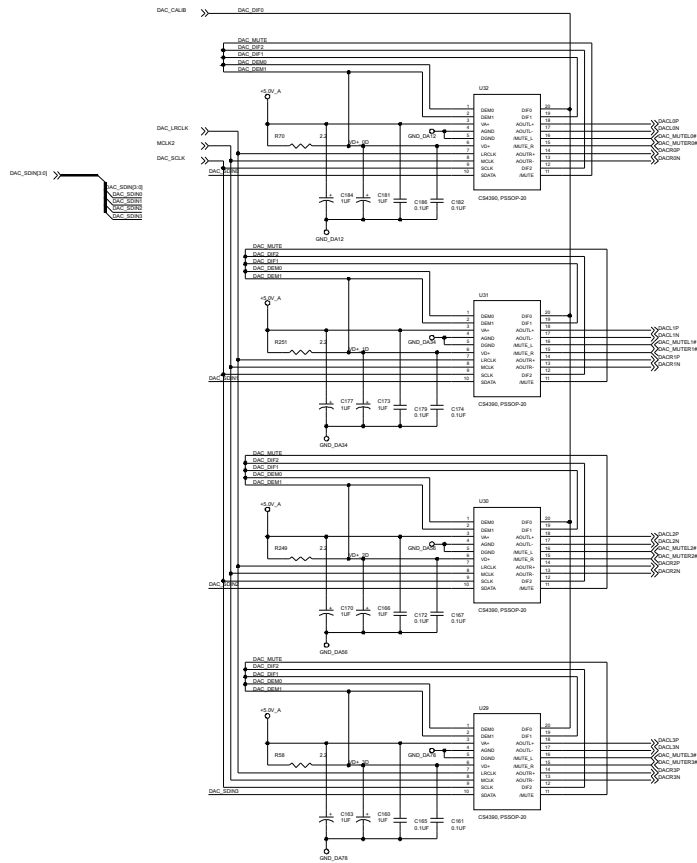


Vih 2.4V min  
Vil 0.8V max

Voh (VD+) = 1.0 V min (4.0V min)  
Vol 0.4V max

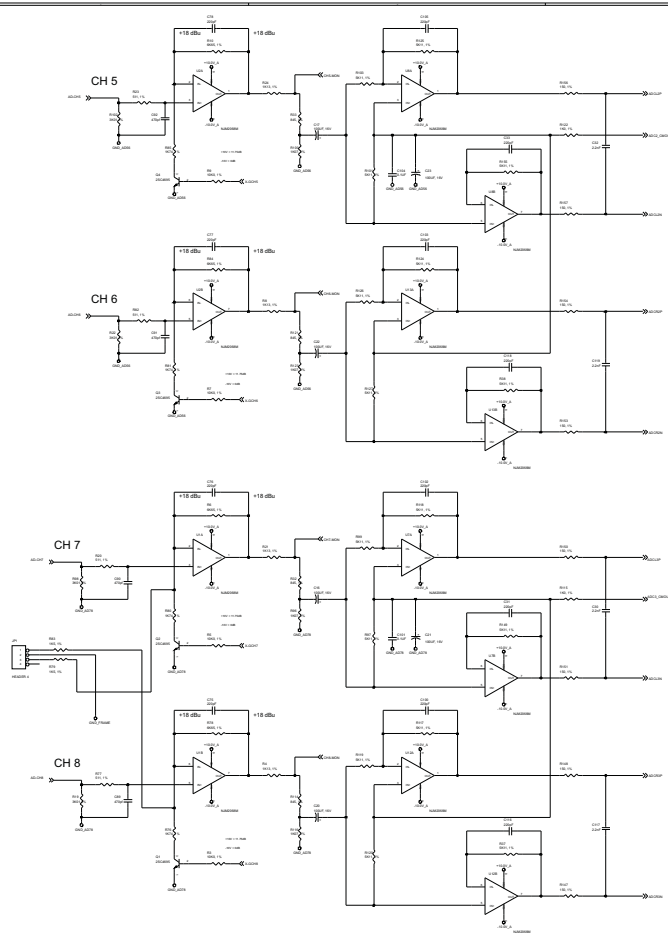
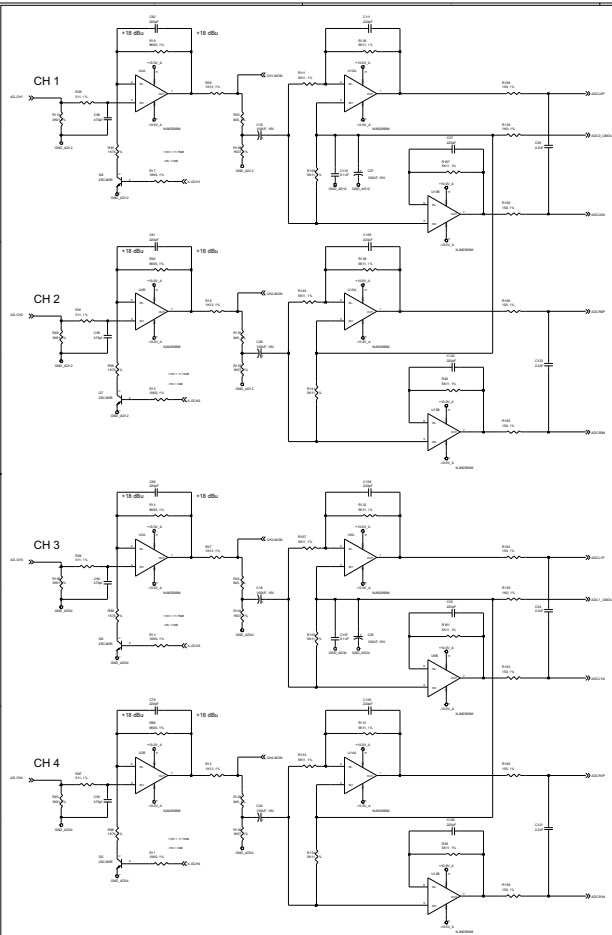


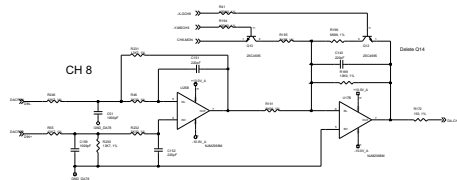
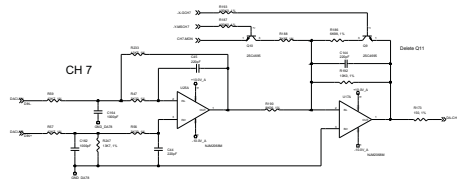
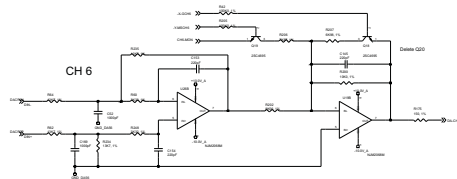
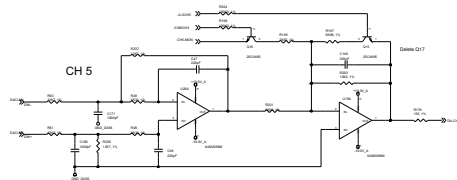
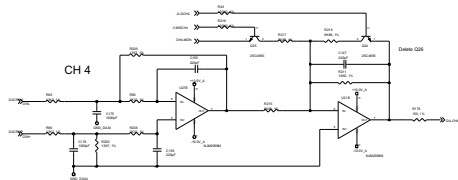
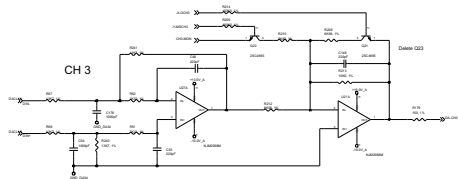
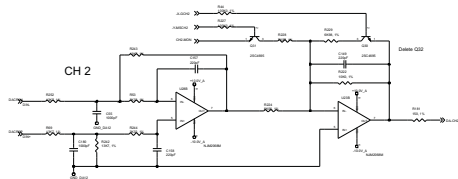
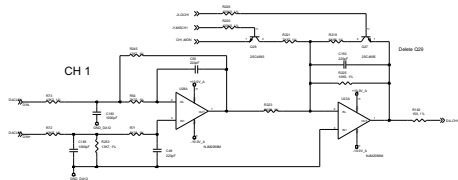
Digital Characteristics  
Vih 2.4V min  
Vih 5.5V max  
Voh - no digital outputs  
Vol - no digital outputs



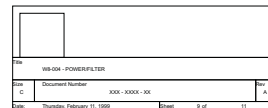
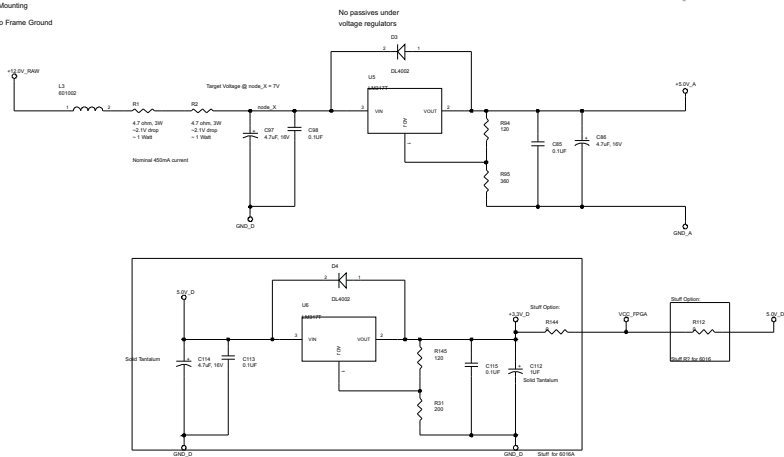
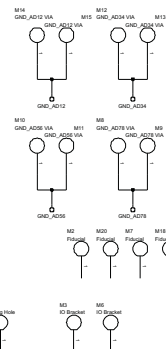
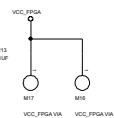
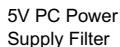
\* From TI dataset

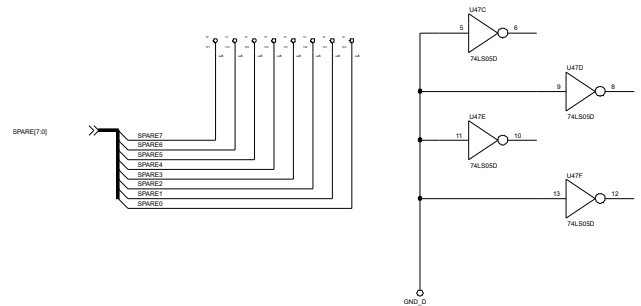
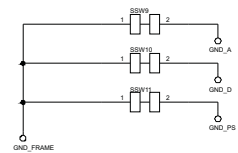
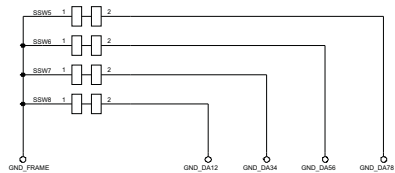
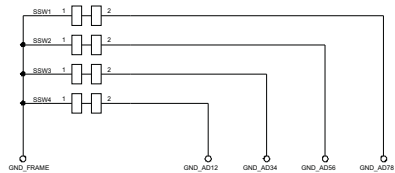




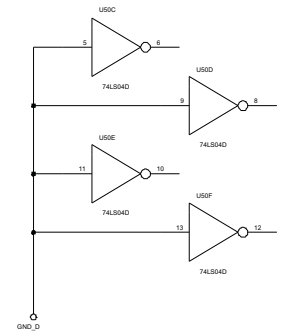






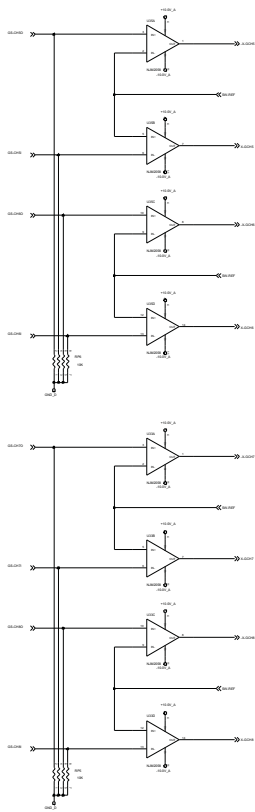


# Spare gates



Title		WS-004 - TEST HEADERS	
Box	Document Number	XXXX - XXXX - XX	Rev
B			A
Date		Thursday, February 11, 1999	Sheet
		10	11

>2.4V = -10dBV INPUT  
<2.4V = +4dBV INPUT



>2.4V = MONITOR MODE  
<2.4V = CONVERTER MODE

